TECHNICAL SPEC FOR Track system

System Model:
TEL Clean Track Mk8Z

Tool status :

The tool, inclusive photoresist pumps, and the bypack units have been flushed and cleaned with solvent/DI water, and dry cleaned with CCA. The solvent lines (HMDS, Resist, thinner, developer have been disconnected from the tool), as well as the electric power supply lines. The bypacks of the tool are situated in the basement under the fab. The water cooling line and signal cables are still connected between process unit and bypack.
Decommissioning was done by trained Litho technicians.

Specific Model: MD8271114

Wafer size: 6 inch

Wafer type: Jeida flat

Wafer flow:

Number coater/developer modules: 2C/3D

InLine system: Yes

Resists / Coaters: 4 (mini)

E2 Develop nozzle kits: 3

RRC Pumps & kits:

Dual-stream nozzles / Developer: NA

Solvent & developer auto supply systems: yes

Temperature / Humidity controller: Yes

Configuration/layout of each block: See below

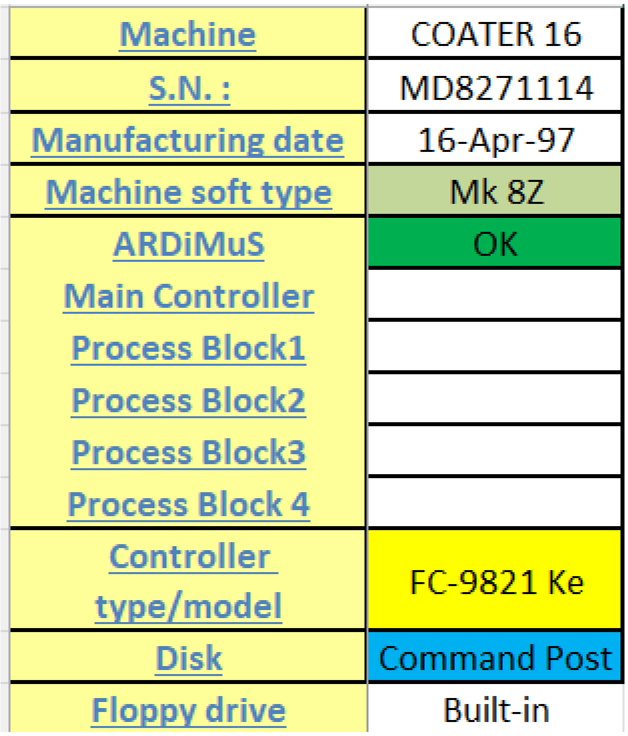
GEM: yes

AVG:

HMDS Bubblers: venturi

Power supply: 208 V, 3 Phase, AC Transformer:

Vintage: 1997

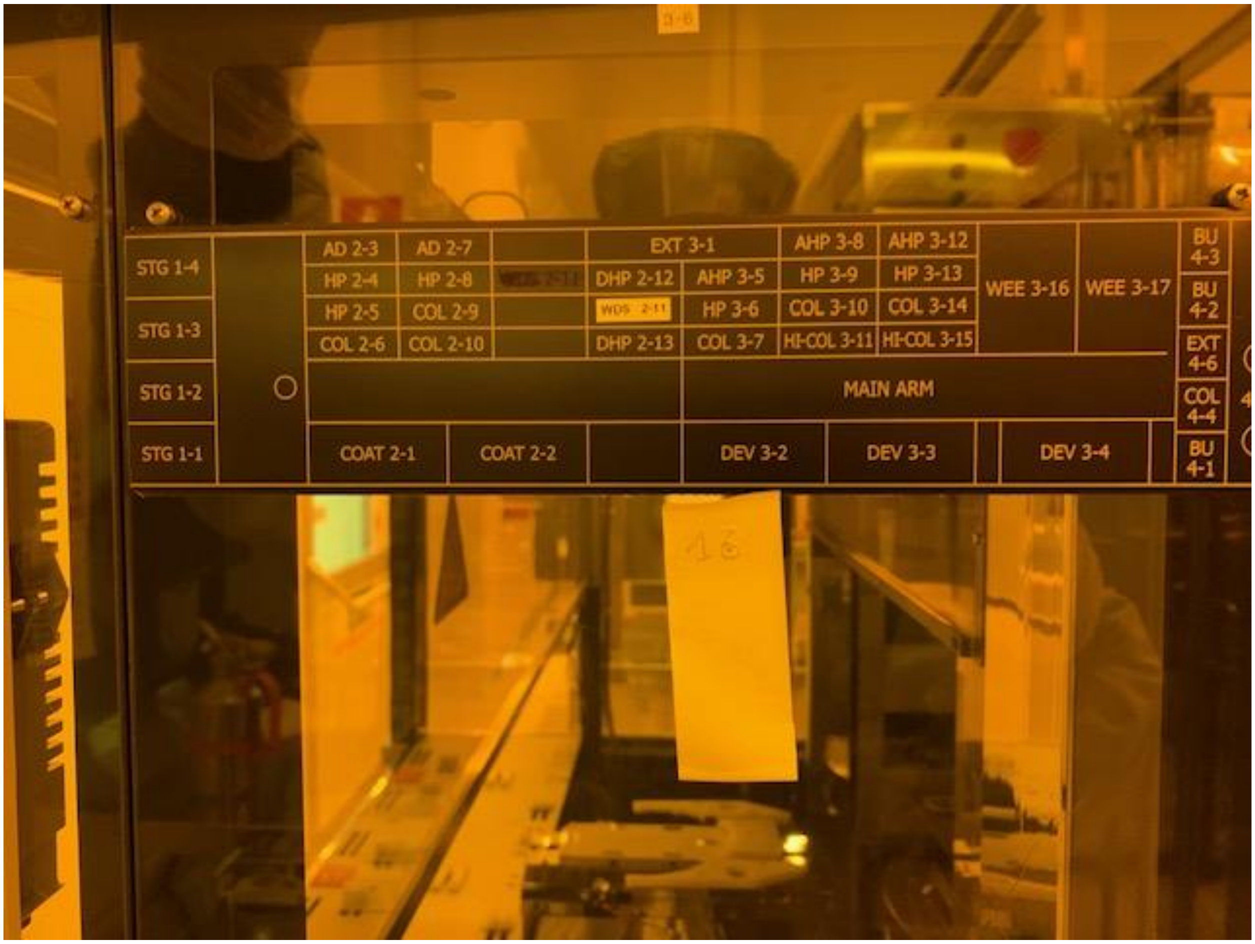


Machine	COATER 16
S.N. :	MD8271114
Manufacturing date	16-Apr-97
Machine soft type	Mk 8Z
ARDiMuS	OK
Main Controller	
Process Block1	
Process Block2	
Process Block3	
Process Block 4	
Controller type/model	FC-9821 Ke
Disk	Command Post
Floppy drive	Built-in

Missing parts: none

Defected parts: 1 temperature controller is defective

Operating system:


STG 1-4
STG 1-3
STG 1-2
STG 1-1
AD 2-3
AD 2-7
HP 2-4
HP 2-8
HP 2-5
COL 2-9
COL 2-6
COL 2-10
EXT 3-1
DHP 2-12
WDS 2-11
DHP 2-13
AHP 3-5
HP 3-6
COL 3-7
AHP 3-8
HP 3-9
COL 3-10
HI-COL 3-11
AHP 3-12
HP 3-13
COL 3-14
HI-COL 3-15
WEE 3-16
WEE 3-17
BU 4-3
BU 4-2
EXT 4-6
COL 4-4
BU 4-1
MAIN ARM
COAT 2-1
COAT 2-2
DEV 3-2
DEV 3-3
DEV 3-4
16

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《 ASSIST 》 3226
ALARM
13/11/20 16: 3:2
1.Config
2. Flow
3. Lot
4.Recipe
5. Data
6.Status
2-11 WDS
2-12 H OVEN
2-13 H OVEN
3-1 EXT
3-8 A OVEN
3-12 A OVEN
3-16 WEE 5
3-17 10 WEE 5
3-5 A OVEN
3-9 L OVEN
3-13 L OVEN
3-6 L OVEN
3-10 T.COOL
3-14 T.COOL
3-7 T.COOL
3-11 T.COOL
3-15 T.COOL
3-2 DEV
3-3 DEV
3-4 DEV
4-2 14 BUFFER
4-3 BUFFER
4-6 I Cool
4-4 I Cool
4-1 BUFFER
4-5 12 WFSTG7
11
#-$$%% #:Block No. $:Unit No. %:Wafer No
&&&&&& &:Unit name
Status (D)
Operate(E)
Register(F)
A
B
C
D
E
F
MENU
ALM
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